



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: KONDO, et al. Examiner: V. Nguyen
Serial No.: 09/852,839 Group Art Unit: 2858
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Due Date: n/a Date:
Title: SEMICONDUCTOR INTEGRATED CIRCUIT WITH NEGATIVE VOLTAGE GENERATION CIRCUIT, TEST METHOD FOR THE SAME, AND RECORDING DEVICE AND COMMUNICATION EQUIPMENT HAVING THE SAME

CERTIFICATE UNDER 37 CFR 1.10:

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